

FIGURE 1

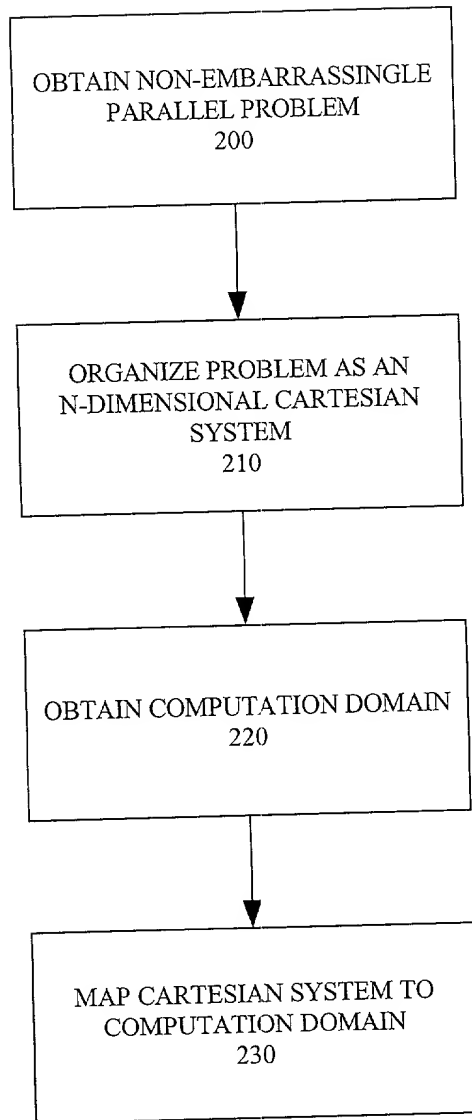


FIGURE 2

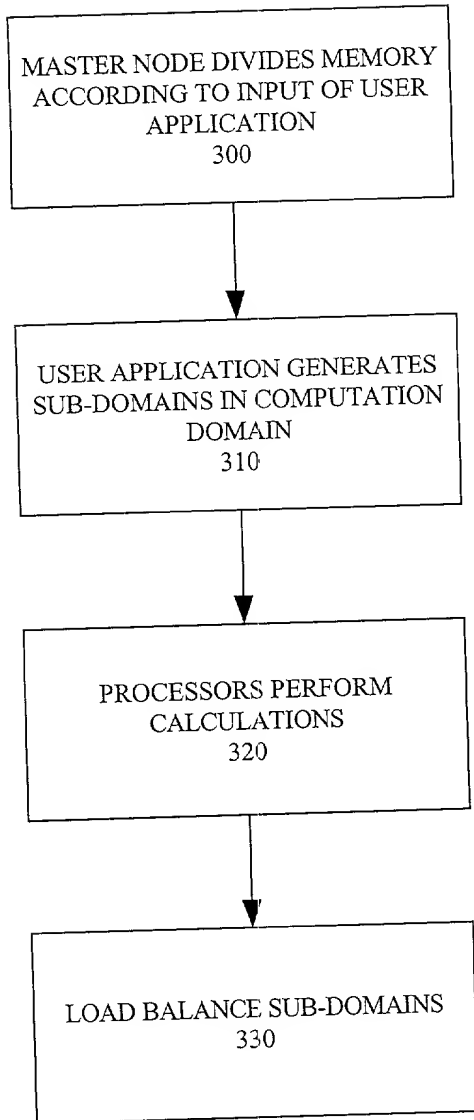


FIGURE 3

```

graph TD
    400[MASTER NODE MEASURES SPEED  
AND MEMORY CAPABILITIES OF  
SLAVE NODES  
400] --> 410[MASTER NODE ASSEMBLES LIST  
OF CPUS  
410]
    410 --> 420[SELECT SUB-DOMAINS OF  
OVERALL N-BOX  
420]
    420 --> 430[ASSIGN CPUS TO SUB-DOMAINS  
430]
    430 --> 440[ASSIGN PROCESS ID TO EACH CPU  
440]

```

FIGURE 4

MASTER NODE MEASURES SPEED
AND MEMORY CAPABILITIES OF
SLAVE NODES
500

MASTER NODE ASSEMBLES LIST
OF CPUS IN COMPUTATION
DOMAIN
510

PARTITION DOMAIN ALONG
LARGEST DIMENSION
515

CONTINUE PARTITIONING
DOMAIN RECURSIVELY
520

START UP SLAVE CPUS
530

CALL ALLOCATION AND
INITIALIZATION ROUTINE ON
FRAGMENTS
540

PERFORM CLIENT FUNCTIONS
550

COMMENCE TIME STEPPING
580

DETERMINE WHICH FIELDS MUST
BE MOVED AROUND
570

PERFORM TIME STEP
INITIALIZATION
560

FIGURE 5

Downloaded from

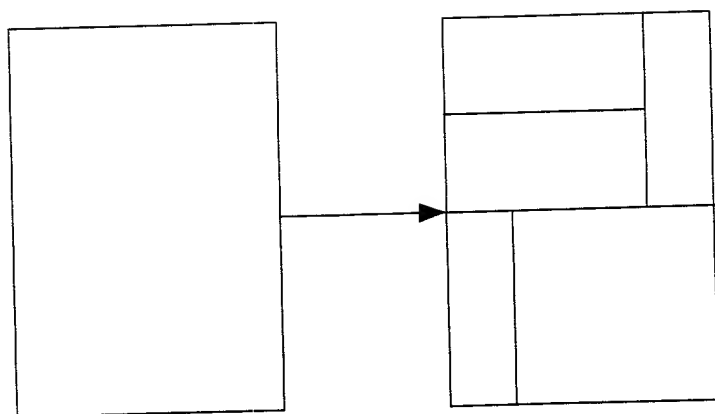
[illegible]

FIGURE 6

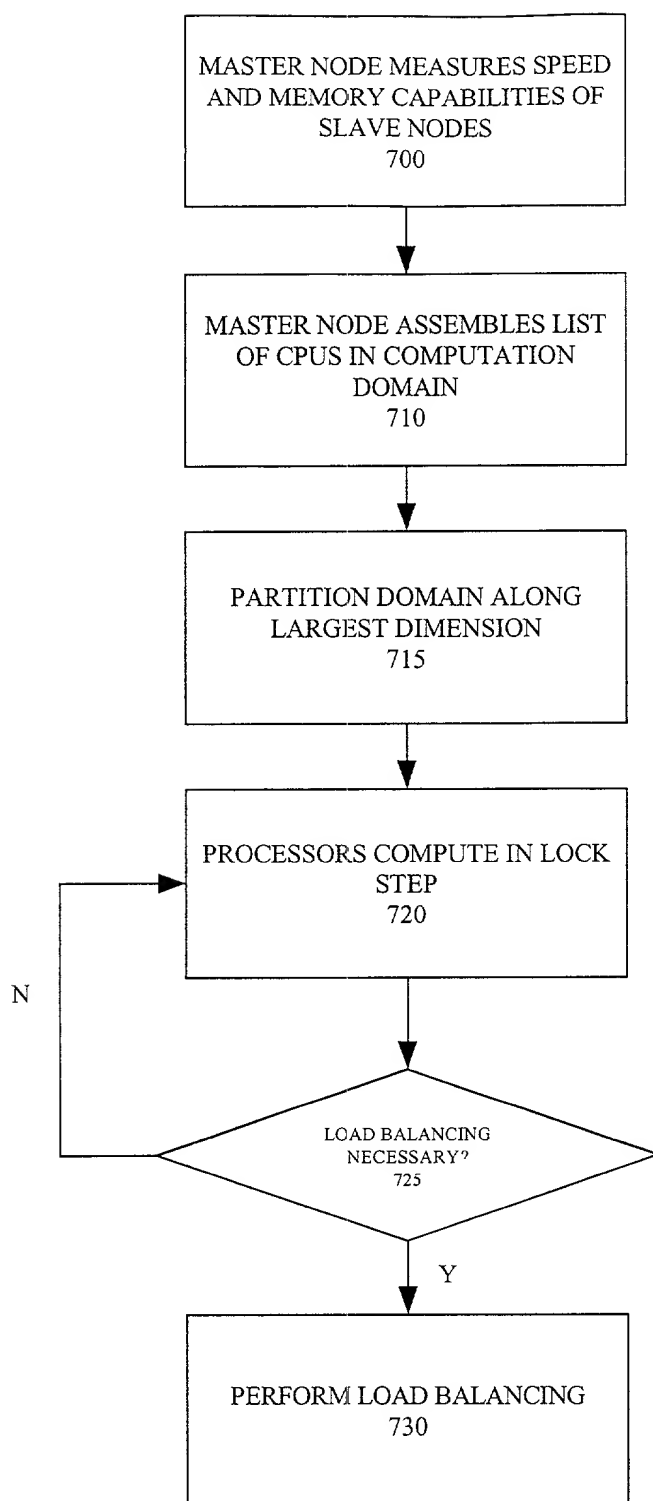


FIGURE 7

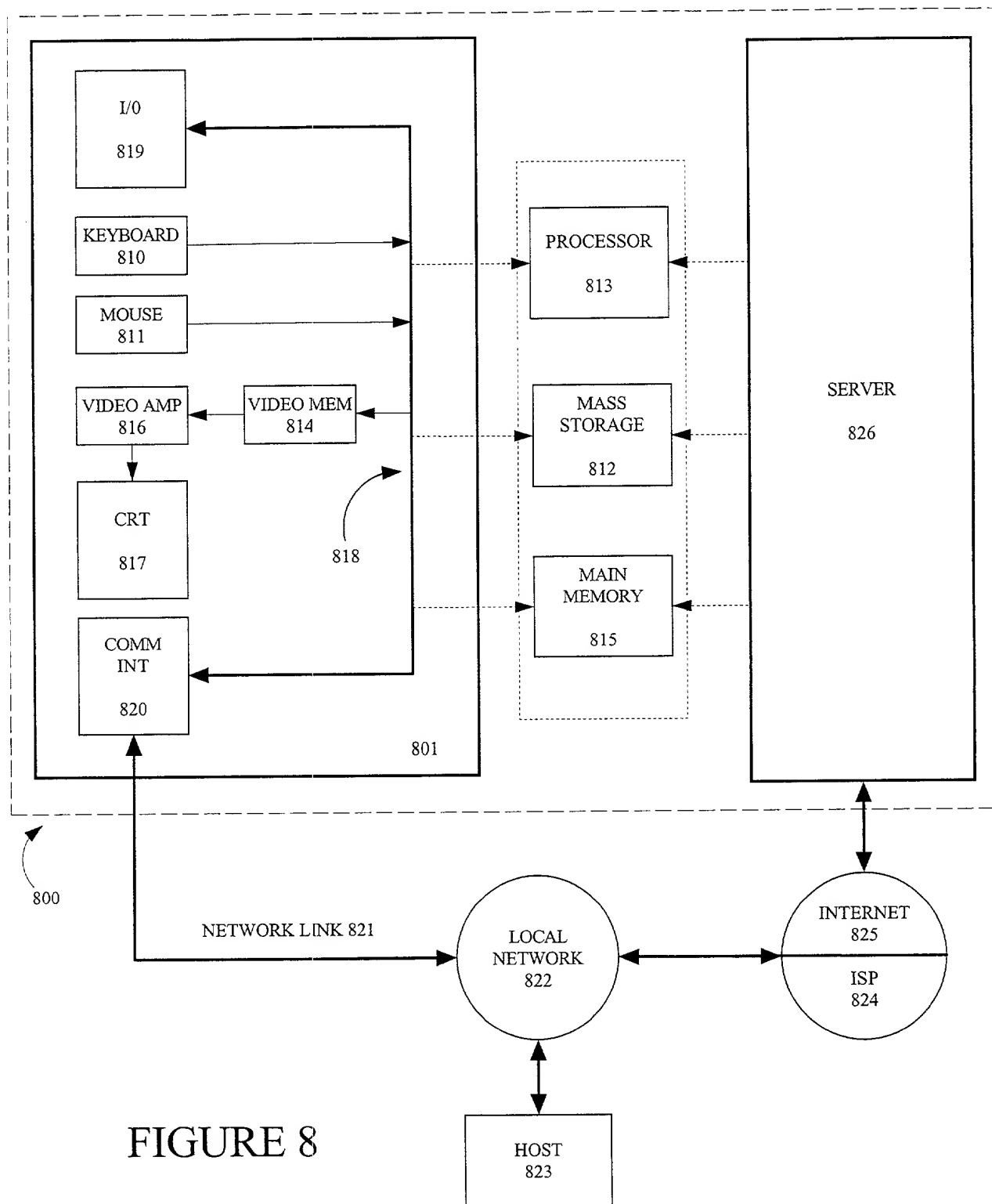


FIGURE 8

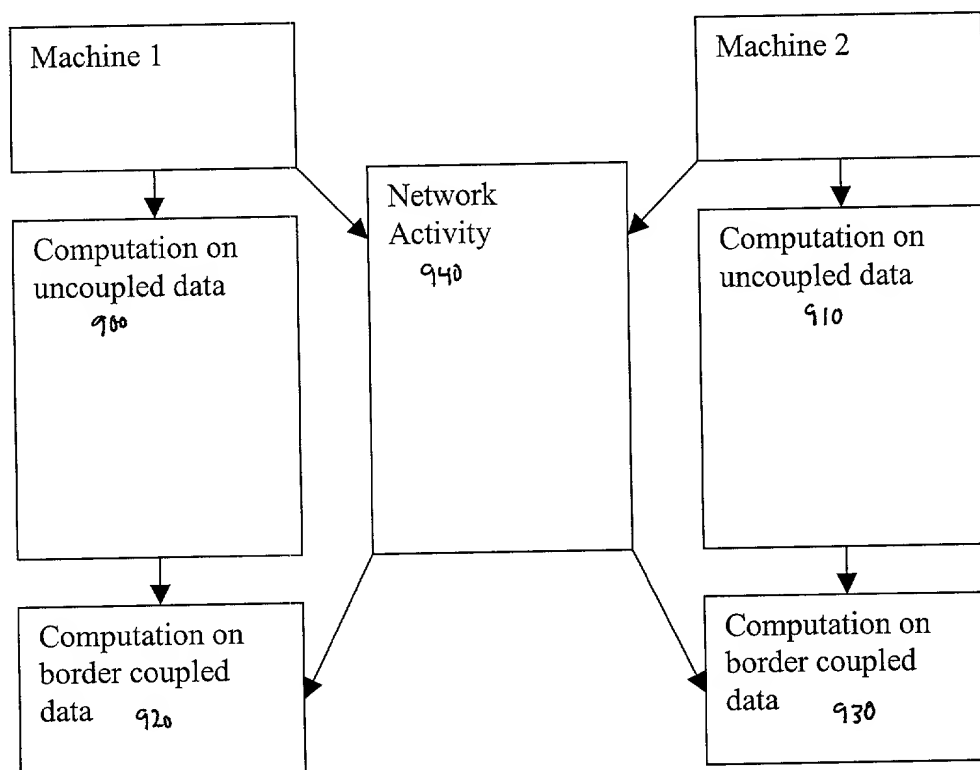


Figure 9